

Amendments to the Specification:

Please delete the Title beginning on page 1, line 1 and replace with the following:

**[[A]] METHOD, APPARATUS, AND COMPUTER
PROGRAM FOR EXECUTING A PROGRAM BY INCORPORATING THREADS**

Please delete the paragraph beginning on page 2, line 11 and replace with the following:

There are a number of branch prediction techniques known in the industry. Such techniques are common in RISC and processor architectures (e.g. The pSeries architecture).

~~See also www.mtl.t.u-tokyo.ac.jp/~niko/Downloads/chitaka-EuroPar2001-PerThreadPredictor.pdf which presents a hardware scheme for improving branch prediction accuracy.~~

Please delete the paragraph beginning on page 2, line 20 and replace with the following:

Software schemes also exist. A paper “Static Correlated Branch Prediction” by Cliff Young and Michael D Smith (ACM Transactions on Programming Languages and Systems, Vol. 21, Issue 5 (Sept 1999), pages 1028-1075, ~~No 2, ??? 1999, Pages 111-159~~) describes how the repetitive behaviour in the trace of all conditional branches executed by a program can be exploited by a compiler. Another paper “A Comparative Analysis of Schemes for Correlated Branch Prediction” by Cliff Young, Michael D Smith and Nicholas Gloy (published in the Proceedings of the 22nd Annual International Symposium on Computer Architecture, June 1995) presents a framework that categorizes branch prediction schemes by the way in which they partition dynamic branches and by the kind of predictor they use.

Please delete the paragraph beginning on page 4, line 4 and replace with the following:

It is also known for the programmer to be able to provide branch prediction pragma—see <http://www.geocrawler.com/archives/3/357/1993/7/0/1992785/>.

Please delete the Title beginning on page 17, line 1 and replace with the following:

**[[A]] METHOD, APPARATUS, AND COMPUTER
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